

In the claims:

For the Examiner's convenience, all pending claims are presented below with changes shown. Please cancel claims 2-11 and 25-35 without prejudice.

1. (Currently Amended) A radio frequency tuner comprising:
an Inter-Integrated circuit (I2C-bus);
a frequency changer including:
a mixer stage to receive a radio frequency (RF) input and having a performance and a gain which are first and second functions of a power consumption; and
a bus interface, coupled to the I2C bus, having an input coupled to a data bus; and
a microcontroller, coupled to the I2C bus, to control a variable gain arrangement of the mixer stage via the bus interface to compensate at least partly for a variation of said gain of ~~said~~ at least one stage; and
a comparator for comparing a tuner performance with a first predetermined performance and for causing said microcontroller to reduce said power consumption of said at least one stage when said tuner performance exceeds said first predetermined performance.

2-11. (Cancelled)

12. (Currently Amended) A tuner as claimed in claim 1 ~~11~~, in which said comparator is arranged to compare said tuner performance with a second predetermined performance

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lower than said first predetermined performance and said microcontroller is arranged to increase the power consumption of said at least one stage when said tuner performance is less than said second predetermined performance.

13. (Currently Amended) A tuner as claimed in claim 1 ~~11~~, in which said tuner performance is a ratio of signal to noise plus intermodulation products.

14. (Currently Amended) A tuner as claimed in claim 1 ~~11~~, comprising a digital tuner.

15. (Original) A tuner as claimed in claim 14, in which said tuner comprises a demodulator and said tuner performance is a bit error rate.

16. (Currently Amended) A tuner as claimed in claims 1 ~~11~~, in which said first predetermined performance is greater than or equal to an acceptable minimum performance.

17. (Original) A tuner as claimed in claim 12, in which said second predetermined performance is less than or equal to an acceptable minimum performance.

18. (Original) A tuner is claimed in claim 15, in which said bit error rate is an instantaneous bit error rate.

19. (Original) A tuner as claimed in claim 15, in which said bit error rate is a time-averaged bit error rate.
20. (Original) A tuner as claimed in claim 15, in which said bit error rate is a combination of an instantaneous bit error rate and a time-averaged bit error rate.
21. (Currently Amended) A tuner as claimed in claim 1 ~~11~~, in which said comparator is arranged to perform a comparison continuously.
22. (Currently Amended) A tuner as claimed in claim 1 ~~11~~, in which said comparator is arranged to perform a comparison periodically.
23. (Currently Amended) A tuner as claimed in claims 1 ~~11~~, in which said comparator is arranged to perform a comparison each time said tuner is powered up.
24. (Currently Amended) A tuner as claimed in claim 1 ~~11~~, in which said comparator is arranged to perform a comparison each time a change of a tuned frequency is requested.
- 25-35. (Cancelled)